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" logic design" vs. " digital circuit design" -

To me, the job titles are very similar, but not exactly the same. "Logic design," in my opinion, implies writing VHDL or Verilog to design digital logic to go onto

The verilog hardware description language (fifth

originally developed for logic simulation. The design of the hardware design languages like VHDL and Verilog provide used in digital circuit design.

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